IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Paul Elliott
Application No. : 10/658,595

Filed : September 9, 2003

For : BRIDGE CIRCUIT FOR USE IN RETIMING IN A

SEMICONDUCTOR INTEGRATED CIRCUIT

Examiner : Nimesh G. Patel

Art Unit : 2111

Docket No. : 851963.413

Date : November 4, 2008

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT

Commissioner for Patents:

In response to the Office Action dated August 4, 2008, please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.